IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Crocker et al.

Attorney Docket No.: CISCP131/1842

Application No.: 09/490,761

Examiner: Matthew R. Demicco

Filed: January 24, 2000

Group: 2697

Title: TECHNIQUE FOR SYNCHRONIZING MULTIPLE ACCESS CONTROLLERS AT

THE HEAD END OF AN ACCESS

NETWORK

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on January 6, 2006 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450

Alexandria, VA 22/13-1450.

SEPARATE LETTER TO THE OFFICIAL DRAFTSMAN REQUESTING ENTERING OF FORMAL DRAWINGS (MPEP 608.02(p))

Mail Stop Office of Publication Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the objections made due to informalities in the original drawings, enclosed are substitute (formal) drawings for the above-identified patent application. If the Draftsman has any question concerning these drawings, he or she is respectfully requested to contact the undersigned.

Respectfully submitted,

BEYER WEAVER & THOMAS, LLP

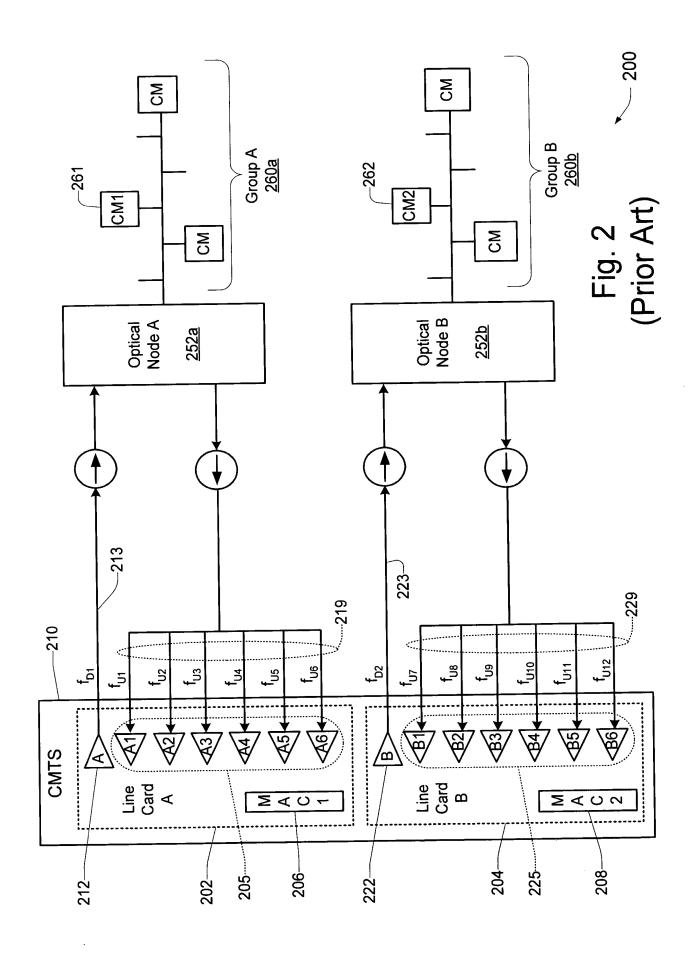
Bean E. Wolf

Registration No. 37,260

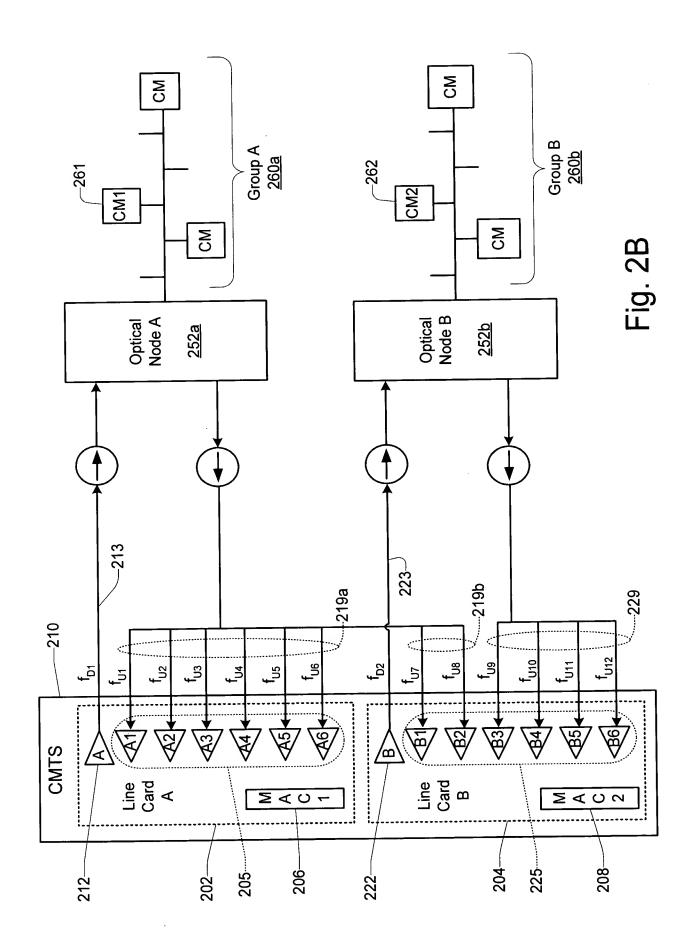
P.O. Box 70250 Oakland, CA 94612-0250 (510) 663-1100

App. No: 09/490,761 Filed: 01/24/2000 Atty Dkt: CISCP131 Sheet 1 of 12 REPLACEMENT FORMAL DRAWINGS Cable Modem Figure 1 0000 50 Fiber 102 106 ATAN MICHAEL Cable Modem Termination System (CMTS) Head End 104 Internet 100

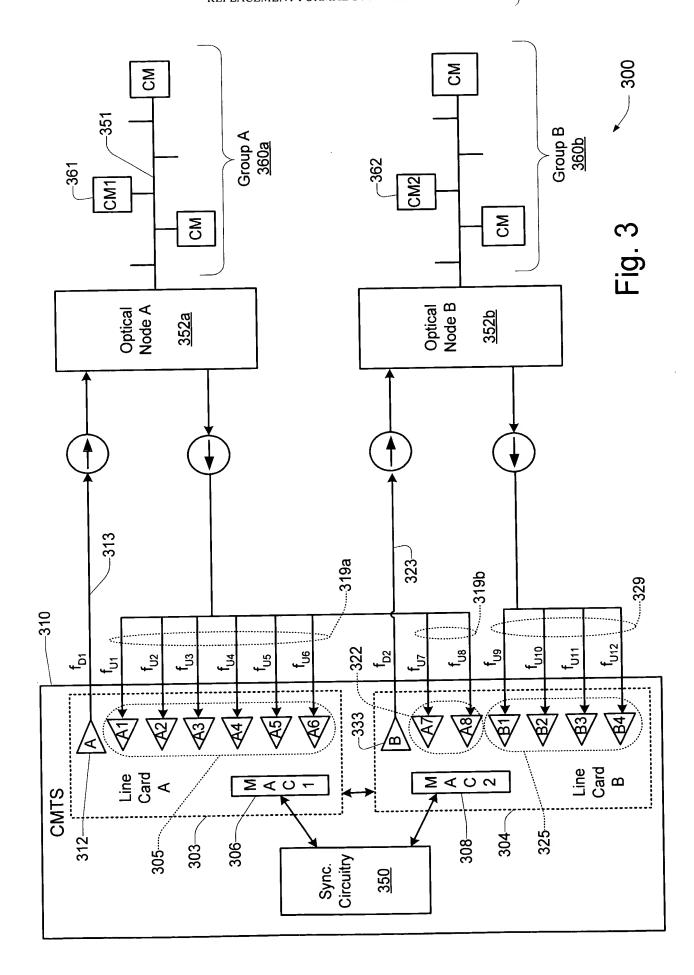
App. No: 09/490,761 Filed: 01/24/2000 Atty Dkt: CISCP131 Sheet 2 of 12 REPLACEMENT FORMAL DRAWINGS



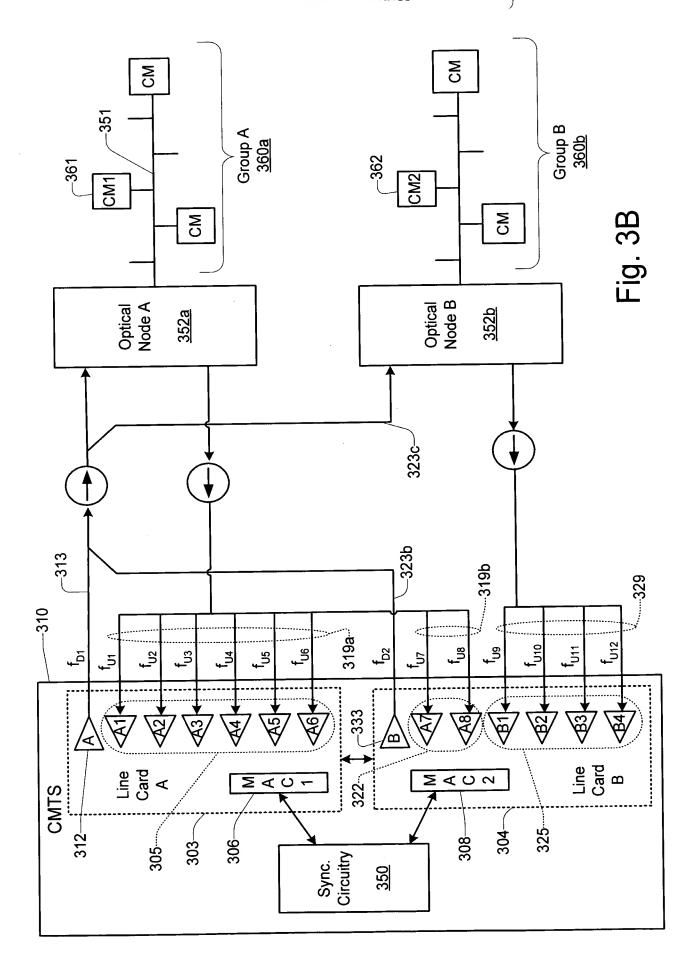
App. No: 09/490,761 Filed: 01/24/2000 Atty Dkt: CISCP131 Sheet 3 of 12 REPLACEMENT FORMAL DRAWINGS



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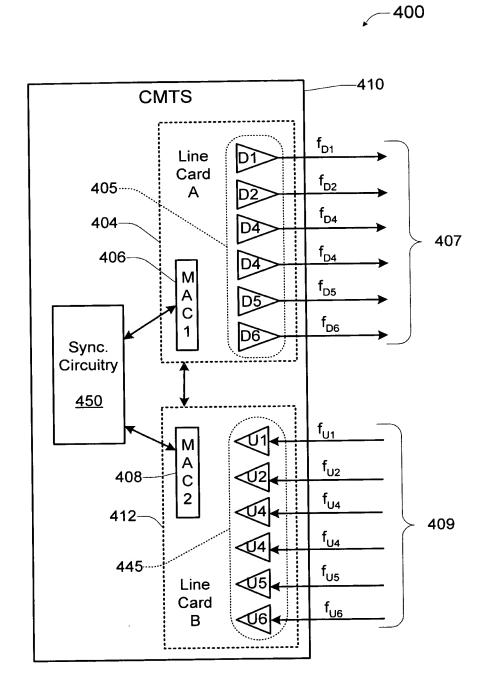
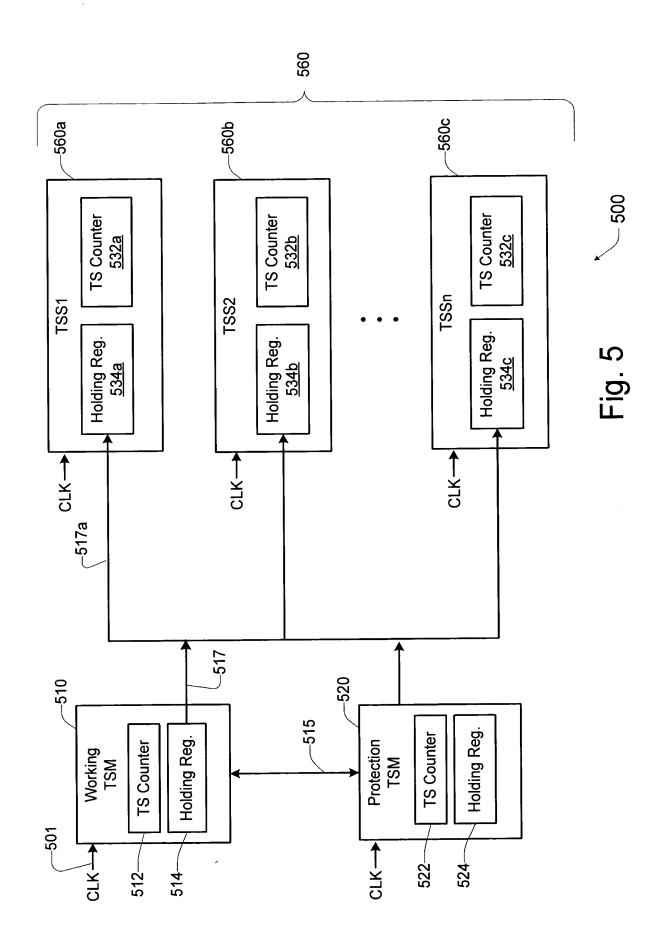


Fig. 4



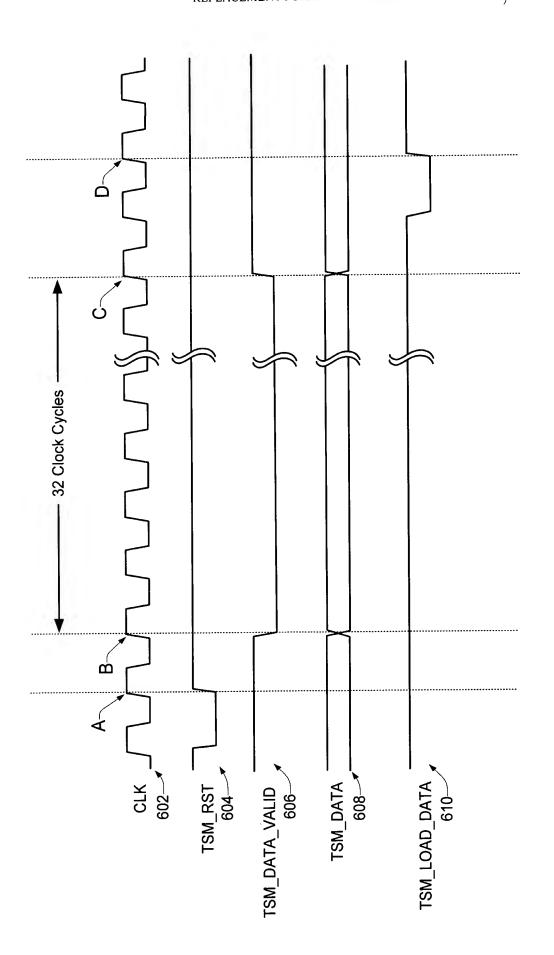


Fig. 6

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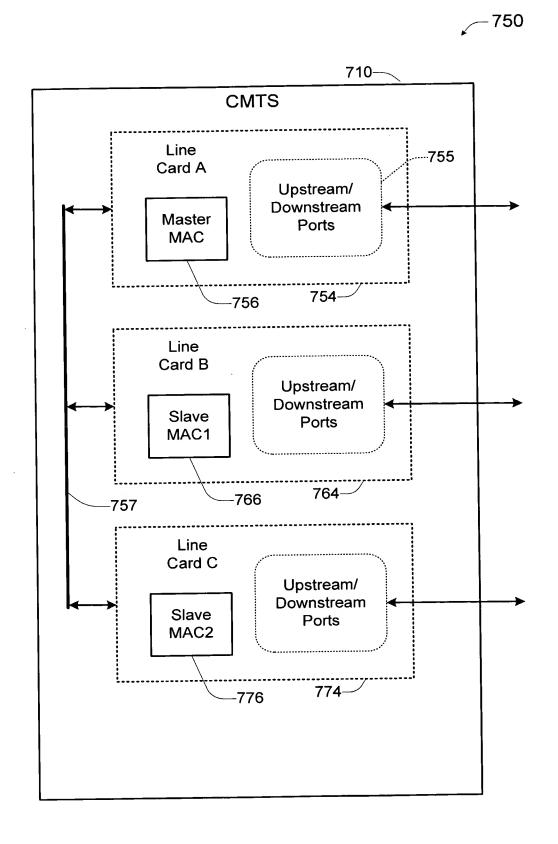
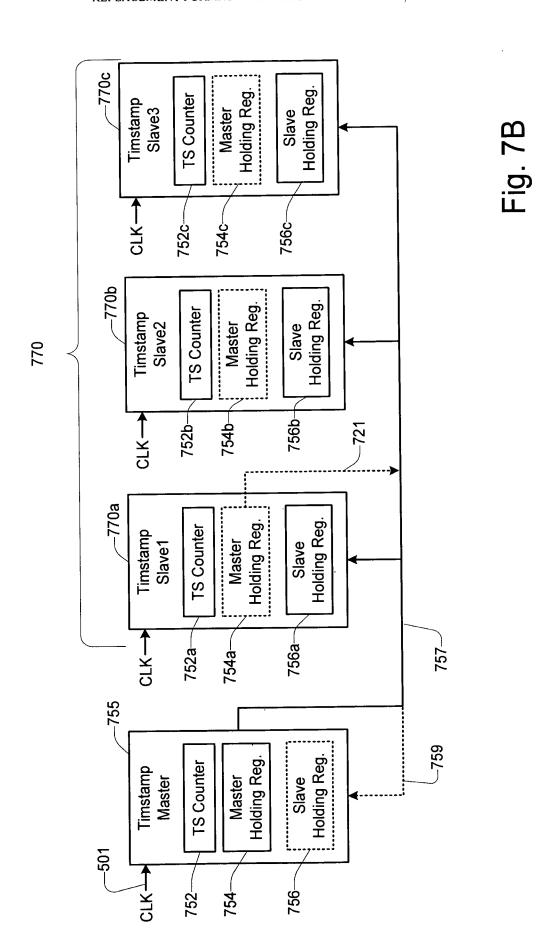


Fig. 7A



App. No: 09/490,761 Filed: 01/24/2000 Atty Dkt: CISCP131 Sheet 11 of 12 REPLACEMENT FORMAL DRAWINGS 802-Interface Network Data 850-882-Switching SW Processor(s) 884 **DOCSIS MAC SW** MEDIA ACCESS CONTROL MAC Layer 830 DOCSIS MAC HW **-855 DOCSIS PHY HW** 806 Memory and Receiver Downstream Demodulator Transmitter Physical Layer Modulator Upstream DOCSIS PHY **DRIVER SW** 886and 832 -857 **-804** 820 Combiner 812-Figure 8 808-Fiber Node

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